

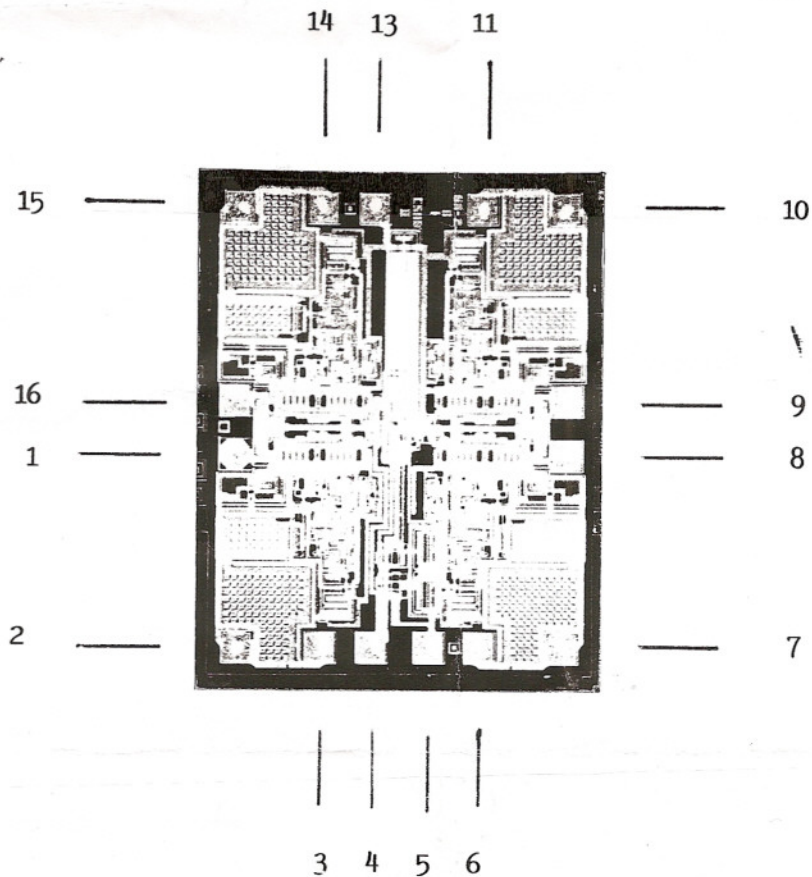


# Sierra Components, Inc.

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Chip back potential is the level which bulk silicon is maintained by on-chip connection, or it is the level to which the chip back must be connected when specifically stated below. If no potential is given the chip back should be isolated.



**Topside Metal: Al**  
**Backside: Silicon**  
**Backside Potential:**  
**Mask Ref: -**  
**Bond Pads : -**

**APPROVED BY: CW**  
**MFG: Siliconix**

**DIE SIZE : .64" x .81"**

**DATE: 1/6/04**  
**P/N: DG271**